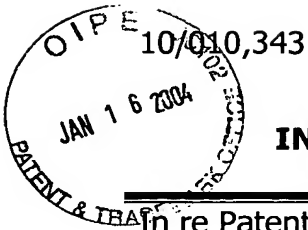


PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

Visvamohan Yegnashankaran et al.

Appln. No.: 10/010,343

Filed: December 5, 2001

For: INTEGRATED CIRCUIT AND METHOD
OF FORMING THE INTEGRATED
CIRCUIT HAVING A DIE WITH HIGH Q
INDUCTORS AND CAPACITORS
ATTACHED TO A DIE WITH A
CIRCUIT AS A FLIP CHIP

Group Art Unit: 2826

Examiner: L. Andújar

RESPONSE TO OFFICE ACTION DATED
AUGUST 21, 2003 AND ADVISORY ACTION
DATED DECEMBER 16, 2003

INTRODUCTORY COMMENTS

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action mailed August 21, 2003 and the Advisory Action mailed December 16, 2003, please amend the above-identified applications as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper;

Remarks which begin on page 10 of this paper; and

An **Appendix** including a replacement drawing sheet of FIG. 1 is attached following page 16 of this paper.

RESPONSE TO OFFICE ACTION
DATED AUGUST 21, 2003 AND ADVISORY
DATED DECEMBER 16, 2003

Atty. Docket No. 100-16301
(P05089-C1)